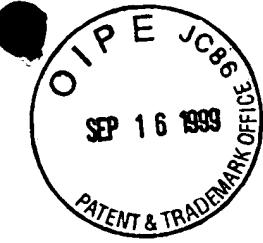


S/N 08/984,563



PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Jeffrey S. Mailloux et al.

Examiner: Hong Kim

Serial No.: 08/984,563

Group Art Unit: 2751

Filed: December 3, 1997

Docket: 303.623US4

Title: ASYNCHRONOUSLY-ACCESSIBLE MEMORY DEVICE WITH MODE  
SELECTION CIRCUITRY FOR BURST OR PIPELINED OPERATION

**PRELIMINARY AMENDMENT**

Box CPA  
Assistant Commissioner for Patents  
Washington, D.C. 20231

Before taking up the present application for examination, please amend the application as follows:

**IN THE CLAIMS**

Please amend the claims as follows:

1       36. (Amended) A method for accessing an asynchronously-accessible dynamic random  
2       access memory, comprising [the steps of]:  
3                 receiving an external row address to the asynchronously-accessible dynamic  
4       random access memory;  
5                 selecting between a burst mode and a pipelined mode of operation of the  
6       asynchronously-accessible dynamic random access memory;  
7                 selecting between a read operation and a write operation of the asynchronously-  
8       accessible dynamic random access memory; and  
9                 obtaining a first external column address for accessing the asynchronously-  
10      accessible dynamic random access memory.

1       37. (Amended) The method, as in Claim 36, further comprising [a step of]:  
2                 obtaining a second external column address subsequent to the first external  
3       column address for operation in the pipelined mode.

RECEIVED  
SEP 21 1999  
T.M. MAIL ROOM

10/22/99  
J/K